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THE UNITED STATES PATENT AND TRADEMARK OFFICE

MAY 0 6 2004

In re Application of:

Peyman Hadizad

erial No.:

10/623390

Filing Date:

July 18, 2003

Group Art Unit:

Unknown

Examiner:

Unknown

Title:

DC/DC CONVERTER WITH DEPLETION MODE COMPOUND SEMICONDUCTOR FIELD

EFFECT TRANSISTOR SWITCHING DEVICE

I HEREBY CERTIFY THAT THIS CORRESPONDENCE IS BEING DEPOSITED WITH THE UNITED STATES POSTAL SERVICE AS FIRST CLASS MAIL IN AN ENVELOPE ADDRESSED TO: COMMISSIONER OF PATENTS AND TRADEMARKS P.O. Box 1450, Alexandria, VA 22313-1450, ON: 2004

Date of Deposit

SEMICONDUCTOR COMPONENTS INDUSTRIES, L.L.C.

Name of Berson Signing Certificate

Name of Assignee 5/3/04

SIGNATURE

DATE

SUBMITTAL OF FORMAL DRAWINGS

Honorable Commissioner of Patents and Trademarks, P.O. Box 1450 Alexandria, VA 22313-1450

Attn: Official Draftsperson

SIR:

Enclosed are THREE (3) sheets of formal drawings for the above-identified patent application. The application was filed with informal drawings; please substitute the enclosed formal drawings for those currently on file. Applicants have not received a formal filing receipt, nor any other communication from the Patent Office, since receipt of our return postcard.

Respectfully submitted,

Peyman Hadizad

Attn; Lydia McNamara ON Semiconductor Law Dept./MD A700 P.O. Box 62890 Phoenix, AZ 85082-2890

Kevin B. Jackson

Attorney

Reg. No. 38,502 Tel. (602) 244-5603 Fax: (602) 244-3169